



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

Maisammaguda, Dhulapally, (Post via Kompally), Secunderabad – 500100


Date: 11-06-2015

II B.Tech II Semester (MR13) Regular Examinations, April/May, 2015

Recounting/Revaluation Results

Sl. No.	Roll No.	Branch	Subject	Type	Ext. Marks
1	13J41A0205	EEE	MANEGERIAL ECONOMICS AND FINANCIAL ANALYSIS	RV	31
2	13J41A0223	EEE	MANEGERIAL ECONOMICS AND FINANCIAL ANALYSIS	RV	26
3	13J41A03G5	ME	PRODUCTION TECHNOLOGY	RV	49
4	13J41A0423	ECE	ELECTRICAL CIRCUIT ANALYSIS	RV	34
5	13J41A0462	ECE	PULSE AND DIGITAL CIRCUITS	RV	29
6	13J41A04E3	ECE	ELECTRICAL CIRCUIT ANALYSIS	RV	40
7	13J41A0530	CSE	OPERATING SYSTEMS	RV	50
8	13J41A0530	CSE	COMPUTER GRAPHICS	RV	42
9	13J41A0539	CSE	DESIGN AND ANALYSIS OF ALGORITHMS	RV	39
10	13J41A0541	CSE	FORMAL LANGUAGES AND AUTOMATA THEORY	RV	36
11	13J41A0565	CSE	DATABASE MANAGEMENT SYSTEMS	RV	35
12	13J41A0575	CSE	DATABASE MANAGEMENT SYSTEMS	RV	41
13	13J41A0575	CSE	COMPUTER GRAPHICS	RV	33
14	13J41A0591	CSE	OPERATING SYSTEMS	RV	31
15	13J41A0591	CSE	DESIGN AND ANALYSIS OF ALGORITHMS	RV	44
16	13J41A05A1	CSE	OPERATING SYSTEMS	RV	32
17	13J41A05G9	CSE	COMPUTER ORGANIZATION	RV	26
18	13J41A05J2	CSE	DESIGN AND ANALYSIS OF ALGORITHMS	RV	36
19	13J41A05K3	CSE	FORMAL LANGUAGES AND AUTOMATA THEORY	RV	27
20	13J41A05K9	CSE	DESIGN AND ANALYSIS OF ALGORITHMS	RV	30
21	13J41A05P9	CSE	DESIGN AND ANALYSIS OF ALGORITHMS	RV	30
22	13J41A1226	IT	DESIGN AND ANALYSIS OF ALGORITHMS	RV	34
23	13J41A2511	MINING	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	RV	26

NOTE: In respect of other candidate there is no change


Chief Controller of Examinations