

# MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

## TIME-TABLE

M.TECH I YEAR I SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, JANUARY - 2017

TIME : 10:30 AM to 1:30 PM

| Date & Day            | CIVIL  |   | EEE   |   | ME                                    |   | ECE   |  | CSE   |
|-----------------------|--|---|---|---|---------------------------------------|---|---|--|---|
|                       | Transportation Engineering   | Geotechnical Engineering                      | Control Engineering & Control Systems         | Power Electronics And Electrical Dirives      | Thermal Engineering                   | Advanced Manufacturing Systems              | DS&CE   | VLSI SYSTEM DESIGN                                 |   |
| 27-01-17<br>Friday    | Traffic Engineering & Management (31201)                                     | Advanced Soil Mechanics (31301)               | Digital Control Systems (32102)               | Power Electronic Control Of Dc Drives (32304) | Advanced Heat & Mass Transfer (33103) | Precision Engineering (33203)               | VLSI Technology And Design (34101)                  | VLSI Technology And Design (34101)                 | Advanced Data Structures And Algorithms (35101)   |
| 30-01-17<br>Monday    | Transport Infrastructure Design (31202)                                      | Advanced Foundation Engineering (31302)       | State And Parameter Estimation Theory (32103) | ----  | Advanced Fluid Mechanics (33104)      | Design For Manufacturing & Assembly (33204) | Advanced Data Communications (34103)                | Cpld & Fpga Architectures And Applications (341C1) | Comuter System Design (35102)                     |
| 01-02-17<br>Wednesday | Pavement Analysis And Design (31204)   | Soil Dynamics And Machine Foundations (31303) | Microcontrollers And Applications (34114)     | ----  | Advanced I.C Engines (331B1)          | Special Manufacturing Process (332A1)       | Cmos Digital Integrated Circuit Design (341B1)      | Cmos Digital Integrated Circuit Design (341B1)     | Advanced Operating Systems (35103)                |
| 03-02-17<br>Friday    | Urban Transportation Policy And Planning For Sustainable Development (31203) | Geo-Environmental Engineering (313A2)         | ----  | ----  | ----                                  | ----  | Microcontrollers For Embedded System Design (34104) | ----   | Distributed Systems (35104)                       |
| 06-02-17<br>Monday    | GROUND IMPROVEMENT METHODS (312A2)   | ----  | ----  | ----  | ----                                  | ----  | Digital System Design (34102)                       | ----   | Software Process And Project Management (351B3)   |
| 08-02-17<br>Wednesday | CONCRETE TECHNOLOGY (312B2)  | ----  | ----  | ----  | ----                                  | ----  | ----  | ----   | Software Architecture And Design Patterns (351B3) |

**NOTE:** Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Date: 27-12-2016

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- 1) HODs (CE,EEE,ME,ECE&CSE) are request to inform students and circulation among staff for necessary action
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Chief Controller of Examinations