



MALLA REDDY ENGINEERING COLLEGE(AUTONOMOUS)

M.Tech I Semester (MR15-2015-16 Admitted Students) Supplementary Examinations, JULY/AUGUST - 2017

TIME-TABLE

TIME : 10: 30 AM to 1:30 PM

Date & Day	CIVIL		MEE		ECE		VLSI SYSTEM DESIGN	COMPUTER SCIENCE AND ENGINEERING
	STRUCTURAL ENGINEERING	ELECTRICAL POWER SYSTEMS	THERMAL ENGINEERING	MACHINE DESIGN	DS&CE	EMBEDDED SYSTEMS		
28/07/17 Friday	Theory of Elasticity (51101)	Advanced Power System Analysis (52101)	Advanced Thermodynamics (53101)	Advanced Mechanics of Solids(53301)	Advanced Data Communications(54103)	ARM Architectures (54301)	--	Data Structures and Algorithm Analysis(55101)
31/07/17 Monday	Structural Dynamics (51102)	Advanced power System Protection (52102)	Advanced I.C Engines (53102)	Advanced Mechanics of Machinery (53302)	CMOS Digital Integrated Circuit Design(54107)	Real Time Operating Systems (54116)	--	Computer System Design (55102)
02/08/17 Wednesday	Advanced Structural Analysis (51103)	Modern Control Theory(52103)	Advanced Fluid Mechanics (53103)	Industrial Robotics(53303)	VLSI Technology and Design(54101)	VLSI Technology and Design (54101)	VLSI Technology and Design(54101)	Computer and Communication Networks (55103)
04/08/17 Friday	Ground Improvement Techniques (51105)	EHV AC Transmission (52104)	Non-Conventional Energy Sources (53104)	Vibration Analysis of Mechanical Systems (53306)	Digital System Design(54102)	Embedded C (54303)	--	Distributed Systems (55115)
07/08/17 Monday	Advanced Concrete Technology (51106)	Power Quality(52106)	Thermal and Nuclear Power Plants(53107)	Advanced Mechanics of composite Materials (53307)	Coding Theory and Techniques (54110)	Coding Theory and Techniques (54110)	Coding Theory and Techniques (54110)	Software Architecture and Design Patterns (55122)
09/08/17 Wednesday	Advanced R.C Design (51109)	Energy Management(53108)	Advanced Optimization Techniques & Applications (50B16)	Mechatronics (53311)	Embedded System Design (54106)	Embedded System Design (54106)	--	Advanced Data base Systems (55125)

NOTE: Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 07-06-2017

Copy to:

- 1) HODs (CE,EEE,ME,ECE&CSE) for information & necessary action.
- 2) PG Engg. Coordinator (College).
- 3) Controller of Examination for information & necessary action.
- 4) To be displayed in all Notice Boards.
- 5) To be read in PG Engg. Classes.

Chief Controller of Examinations