

# MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

Maisammaguda, Dhulapally, (Post Via. Kompally), Secunderabad - 500 100.

## TIME-TABLE

### M.TECH I YEAR II SEMESTER(MR13) SUPPLEMENTARY EXAMINATIONS, JANUARY-2017

**TIME : 10:30 AM to 1:30 PM**

Date & Day	EEE			ME		ECE		CSE	
	Transportation Engineering	Geotechnical Engineering	Control Engineering & Control Systems	Power Electronics And Electrical Drives	Thermal Engineering	Advanced Manufacturing Systems	DS&CE		VLSI SYSTEM DESIGN
28-01-2017 Saturday	Traffic Analysis (31207)	Retaining Structures (31307)	Control system design (32109)	Advanced Digital Signal Processing (34115)	Fuel Combustion & Environment (33107)	Simulation Modeling of Manufacturing Systems (33207)	Low Power VLSI design (34107)	Low Power VLSI design (34107)	Advanced Database Systems (35108)
31-01-2017 Tuesday	Land Use Transportation Modeling (31208)	Earth & Rock Fill Dams and Slope Stability (31308)	Non Linear Systems (321C1)	---	Energy Management (33108)	Quality Engineering in Manufacturing Systems (33208)	Advanced Computer Architecture (35116)	CMOS mixed signal circuit design (341D1)	Storage Area Networks (351C2)
02-02-2017 Thursday	Highway Project Formulation & Economics (31209)	Soil-Structure Interaction (31310)	---	---	Advanced Finite Element Analysis (33109)	---	Design for Testability (34108)	---	Web services and service oriented Architecture (35109)
04-02-2017 Saturday	Pavement Construction, Maintenance and Management (31210)	Rock Mechanics and Engineering (313C2)	---	---	Computational Fluid Dynamics (33110)	---	Embedded real time operating systems (34109)	---	Wireless Networks and Mobile Computing (351D2)
07-02-2017 Tuesday	Intelligent Transportation System (312C1)	---	---	---	Thermal & Nuclear Power Plants (331C3)	---	Digital Signal Processors and Architectures (341D2)	Digital Signal Processors and Architectures (341D2)	Cloud Computing (352D1)
09-02-2017 Thursday	Environmental Impact Assessment for Transportation Projects (312D2)	---	---	---	---	---	System on chip architecture (341C2)	System on chip architecture (341C2)	---

**NOTE:** Any clashes / Omissions in this time-table may be brought to the notice of the undersigned immediately

Dt: 27-12-2016

Copy to:

- 1) HODs (CE,EEE,ME,ECE&CSE) are request to inform students and circulation among staff for necessary action
- 2) Controller of Examination for information & necessary action.
- 3) To be displayed in all Notice Boards.

Chief Controller of Examinations