

DECLARATION

The information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing by the organizing committee. If selected, I shall attend the workshop definitely for the entire duration. I also undertake the responsibility to inform the Coordinator, in case I am unable to attend the workshop.

Place:

Date:

Signature of the Applicant
Name:

AUTHORITY CERTIFICATE

This is to certify that _____ is a Lab Assistant/Technician of our institute and is hereby permitted to attend the one-day skill development workshop on **“LATEST TRENDS in VLSI Design and Implementation Using EDA Tools”**, at Electronics and Communication Engineering department, MREC.

Place:

Date:

Signature of the
Head of the
Institute with Seal

ORGANIZING COMMITTEE

Chief Parton:

Sri Ch. Malla Reddy, MP
Founder Chairman, MRGI

Partons:

Sri Ch. Mahendar Reddy, Secretary, MRGI
Dr. Ch. Bhadra Reddy, President, MRGI

Co-Partons:

Dr. A. Ramaswamy Reddy, Director, MREC
Dr. A. Raveendra, Principal, MREC

Convener:

Dr. M. J. C. Prasad, Professor & Head, ECE Dept.

Co-Ordinators:

Dr. Sk. Fairouz. Professor, ECE
Mr. V. Srinivas, Assistant. Professor, ECE
Mr Narasimha Reddy, Assistant. Professor, ECE

Technical Committee:

Dr. S. Madhu Babu, Associate Professor, ECE
Dr. G.S.K. Gayatri Devi, Associate Professor, ECE
Dr. Syed Jalal Ahmad, Associate Professor, ECE
Dr Manikanda Devaraj , Associate Professor, ECE
Dr.T.Srinivas Reddy , Associate Professor, ECE
Dr. Shankaranand Jha, Associate Professor, ECE
Dr. A. Pradeep Kumar, Assoc. Professor, ECE
Dr Sima Sahu , Associate Professor , ECE
Dr. M Nithin Varma, Assistant Professor, ECE
Mrs C. Silpa, Associate Professor , ECE
Mr G Prasana Kumar , Associate Professor , ECE

A FIVE DAY WORKSHOP

On

**“LATEST TRENDS in VLSI Design and
Implementation Using EDA Tools”**

28th Nov – Dec 2nd 2021

Organized

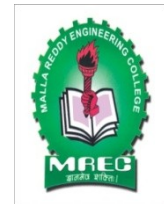
By

Department of ECE

In

ASSOCIATION WITH

XILINX UNIVERSITY & APPLY VOLT



Malla Reddy Engineering College
(An UGC Autonomous Institute under JNTUH)
(Accredited with NAAC ‘A’ Grade)
(NBA Accredited)

Maisammaguda, Dhulapally,
Post Via Kompally, Secunderabad-100
Web site: www.mrec.ac.in

education by encouraging activities addressing the societal needs.

Introduction

Malla Reddy Engineering College a premier institute in Andhra Pradesh was established by Sri. Ch. Malla Reddy in the year 2002 in a sprawling campus of 10 acres as a part of the reputed CMR Educational Society and has been pioneer in imparting quality technical education and mission for eminence in research with social relevance. The college offers four year B.Tech programming in CE, EEE, ME, ECE, CSE and Mining. It also offers PG Courses MBA, MCA and 7 M.Tech Courses in different disciplines. The college is NBA accredited and also accredited with NAAC 'A' Grade. The college obtained autonomous status from the academic year 2011. This college ensured TEQIP funds from WORLDBANK in year 2011. The college has obtained UGC Autonomous status from the academic year 2014-15 for a period of six years.

About ECE Dept:

The Department of Electronics and Communication Engineering established in 2002. The department is supported by 41 teaching faculty members having M.Tech/M.E. qualification and some are having PhD's and experience in academic, industrial and R&D too. The department is having sophisticated laboratories to cater the needs of not only curriculum but also for present day needs. The department has got NBA accreditation. The B.Tech students graduated with 95% results and well settled in Multi National Companies.

VISION:

To produce innovative, globally competent and ethical Electronics and communication engineers to cater socio-economic needs.

MISSION:

To impart quality education in Electronics and Communication Engineering discipline and produce employable graduates. To improve the thought process of students by exposing them to advanced technologies and make them innovative in their career. To provide ethical and value-based

Programme Educational Objectives

PEO I: Graduates acquire insights into mathematics, sciences, computing and fundamentals of electronics and communication engineering including breadth to meet global demand and competitiveness in terms of technological aspects.

PEO II: Graduates excel in best post graduate schools, reaching advanced degrees in engineering and related disciplines; will have skills for continued independent, lifelong learning to become experts in their profession..

PEO III: Graduates possess best communicative skills and work efficiently on team-based projects in electronics, communication, computational and manufacturing firms with a sense of social responsibility.

Workshop Contents:

- **Introduction to Xilinx Vivado Software.**
- **Verilog Programming for all modules.**
- **Simulation.**
- **Synthesis.**
- **Timing Report.**
- **Power Report.**
- **Function Verification using FPGA Kit.**
- **Block level synthesis using HLS Tool**
- **Project**

Target People:

III Year students of ECE Department.

Registration fee: 300

Venue:

VLSI LAB, ECE DEPT., MREC

APPLICATION FORM

1. Name :
2. Date of Birth & Age :
3. Gender (M / F) :
4. Qualification :
5. Branch :
6. Name of the college :
7. Address :

Mobile: _____

E-mail id: _____

8. Any other relevant information:

Address for all Correspondence

**Dr.M.J.C.Prasad, Convener
Profesor & Head, Dept. of ECE
Malla Reddy Engineering College
Maisammaguda, Dhulapally,
Post Via Kompally, Secunderabad-100**

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